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Amendment and/or Response
Reply to Office action of 27 July 2005

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Amendments to the Claims:

A listing of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently amended) A method of tuning a receiver for a digital signal (~~MPEG2-TS~~), the method comprising the steps of:

filtering (~~In-filt, Band-filt~~) an input signal (~~RF-in~~) via one or more filters to obtain a processed signal;

determining (~~Mix/Osc/IF amp, IF downconv-2, Dig-dem, C~~) a digital figure of merit (~~BER~~) from the processed signal; and

fine-adjusting (~~μ P, PLL, DAC1-DAC3~~) the filtering step (~~In-filt, Band-filt~~) a center frequency of at least one of the one or more filters in dependence on the digital figure of merit (~~BER~~).

2. (Currently amended) ~~A~~ The method as claimed in of claim 1, wherein the digital figure of merit (~~BER~~) is a bit-error rate.

3. (Currently amended) ~~A~~ The method as claimed in of claim 1, wherein

the filtering step (~~In-filt, Band-filt~~) ~~comprises~~ includes at least one partial filtering step that is controlled by a control signal; and

the fine-adjusting step (~~μ P, PLL, DAC1-DAC3~~) ~~comprises the step of~~ includes adjusting the control signal in order to optimize the figure of merit.

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4. (Currently amended) ~~A~~ The method as claimed in of claim 1, wherein
the filtering step ~~(In-filt, Band-filt)~~ comprises ~~includes~~ at least two partial
filtering steps that are controlled by at least first and second respective control
signals; and
the fine-adjusting step ~~(μ P, PLL, DAC1-DAC3)~~ comprises the steps of ~~includes~~
adjusting the first control signal in order to optimize the digital figure of merit, to
obtain an adjusted first control signal value, and, while the first control signal is kept
at the adjusted first control signal value, adjusting the second control signal in order
to optimize the digital figure of merit.
5. (Currently amended) ~~A~~ The method as claimed in of claim 1, wherein the filtering
step ~~(In-filt, Band-filt)~~ uses circuitry that is ~~one or more filters are~~ factory pre-aligned.
6. (Currently amended) A receiver for a digital signal ~~(MPEG2-TS)~~, the receiver
comprising:
means for filtering ~~(In-filt, Band-filt)~~ one or more filters that are configured to
filter an input signal (RF-in) to obtain a processed signal;
means for determining ~~(Mix/Osc/IF-amp, IF-downconv-2, Dig-dem, C)~~ a
decoder that is configured to determine a digital figure of merit (BER) from the
processed signal; and
means for fine-adjusting ~~(μ P, PLL, DAC1-DAC3)~~ the filtering means (In-filt,
Band-filt) a controller that is configured to adjust a center frequency of at least one of
the one or more filters in dependence on the digital figure of merit.
7. (New) The receiver of claim 6, wherein the one or more filters include one or more
RF filters.
8. (New) The receiver of claim 6, wherein the digital signal includes an MPEG
encoded signal.

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9. (New) The receiver of claim 6, wherein
the controller includes
a tuner that includes:
a phase-locked-loop and
a plurality of digital-to-analog converters,
each of the digital-to-analog converters being configured to provide a control
signal to each of the one or more filters.
10. (New) The receiver of claim 6, wherein
the one or more filters include:
an input filter, and
a double tuned band-filter.
11. (New) The receiver of claim 10, further including:
a pre-amp that is configured to operably couple the input filter to the band-
filter, and
a mixer that is configured to generate an IF signal from an output of the band-
filter,
wherein,
the decoder is configured to receive the IF signal and to produce therefrom a
digital output signal and the figure of merit.
12. (New) The receiver of claim 6, wherein
the figure of merit includes a bit-error rate.

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13. (New) A method comprising:
receiving an RF input signal,
filtering the RF input signal via one or more RF filters to provide a filtered RF signal,
mixing the filtered RF signal with an oscillator signal to provide an IF signal,
demodulating the IF signal to provide a digital output signal and a figure of merit associated with the digital output signal, and
adjusting at least one filter of the one or more RF filters based on the figure of merit.
14. (New) The method of claim 13, wherein
adjusting the at least one filter includes adjusting a center frequency of the at least one filter.
15. (New) The method of claim 14, further including
adjusting a center frequency of another filter of the one or more RF filters.
16. (New) The method of claim 15, wherein
the adjusting of the center frequency of the at least one filter and the another filter occur sequentially.
17. (New) The method of claim 13, further including
adjusting another filter of the one or more RF filters.
18. (New) The method of claim 17, wherein
the adjusting of the at least one filter and the another filter occur sequentially.

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19. (New) The method of claim 18, wherein
the adjusting of the at least one filter and the another filter are based on a first
control signal and a second control signal that are each independently determined
based on first and second sequences of figures of merit.
20. (New) The method of claim 13, wherein
the figure of merit includes a bit-error rate.

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